

CLAIMS

1. (Original) A method for dynamic emulation of legacy instructions of a legacy program comprising:

- providing state information for determining a program execution mode for emulating said legacy instructions,
- accessing said legacy instructions and said state information,
- for each particular legacy instruction,
- querying to determine if one or more particular translated instructions for said execution mode are stored as a result of translating said legacy instruction for said execution mode,
- and
- if not translated for said execution mode,
- translating the particular legacy instruction into one or more particular translated instructions for emulating the particular legacy instruction for said execution mode,
- storing said one or more particular translated instructions with said state information, and
- if translated for said execution mode, continuing without additional translating,
- accessing said one or more particular translated instructions for emulating said legacy instructions for said execution mode.

2. (Original) The method of Claim 1 wherein said storing of the one or more particular translated instructions is in one or more particular translated blocks and said state information is stored in each of said particular translated blocks.

3. (Original) The method of Claim 1 wherein said legacy instructions are for a legacy system having a S/390 architecture.

4. (Original) The method of Claim 1 wherein said legacy instructions are object code instructions compiled/assembled for a legacy architecture.

1 5. (Original) The method of Claim 1 wherein said translated instructions are for execution in a
2 RISC architecture.

1 6. (Original) A method for dynamic emulation of legacy instructions, where the legacy instructions
2 are compiled/assembled into object code form for a native architecture, where the legacy instructions
3 are executed as guests in the host architecture, where the legacy instructions are translated to
4 translated instructions in the host architecture and the translated instructions are executed in the host
5 architecture concurrently with the translation of the legacy instructions in the host architecture,
6 comprising:

7 providing state information for determining a program execution mode for emulating said legacy
8 instructions,

9 accessing said legacy instructions and said state information as guests in the host architecture,
10 for each particular legacy instruction,

11 querying to determine if one or more particular translated instructions for said execution
12 mode are stored as a result of translating said legacy instruction for said execution mode,
13 and

14 if not translated for said execution mode,

15 translating the particular legacy instruction into one or more particular translated
16 instructions for emulating the particular legacy instruction for said execution
17 mode,

18 storing said one or more particular translated instructions with said state
19 information, and

20 if translated for said execution mode, continuing without additional translating,

21 accessing said one or more particular translated instructions for emulating said legacy
22 instructions for said execution mode as a guest in said host architecture.